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(71) Applicant (for DE only): **PHILIPS INTELLECTUAL PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-damm 94, 20099 Hamburg (DE).

(71) Applicant (for AE, AG, AL, AM, AT, AU, AZ, BA, BB, BE, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CY, CZ, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM,

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(72) Inventor; and

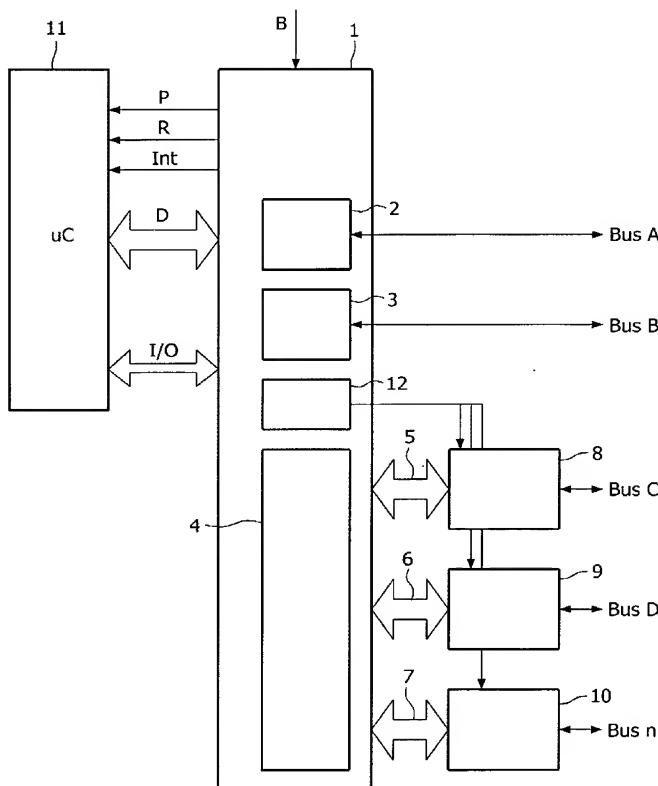
(75) Inventor/Applicant (for US only): **MUTH, Matthias** [DE/DE]; c/o Philips Intellectual Property & Standards GmbH Weiss hausstr. 2, 52066 Aachen (DE).

(74) Agents: **VOLMER, Georg** et al.; Philips Intellectual Property & Standards GmbH Weiss hausstr. 2, 52066 Aachen (DE).

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(54) Title: SCALABLE SYSTEM CHIP FOR COUPLING DATA BUS SYSTEMS



(57) Abstract: The invention relates to a scalable system chip (1) for coupling at least two data bus systems, with at least one transceiver (2; 3), integrated on the system chip, which is provided to create a coupling with a data bus of a first type, and with at least one controller (4), integrated on the system chip, to control at least one external transceiver (8; 9; 10), which is provided for coupling with a data bus of a second type.



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